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PATENT APPLICATION
Q59017

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

4/12/01

V. Hart

In re application of

Shigeru SEMBONMATSU, et al.

Appln. No.: 09/559,348

Group Art Unit: 3728

Confirmation No.: Not yet known.

Examiner: Shian T. Luong

Filed: April 27, 2000

For: TRAY FOR SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

AMENDMENT UNDER 37 C.F.R. §1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated January 23, 2001, please amend the above-identified application as follows:

IN THE CLAIMS:

Please cancel claim 2 without prejudice and/or disclaimer.

Please enter the following amended claim:

RECEIVED
APR 11 2001
TC 3700 MAIL ROOM

11
1. (Amended) A tray for storing a semiconductor integrated circuit device having a package and wiring terminals on a lower surface of the package, said tray comprising:

a substantially planar main body; and

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